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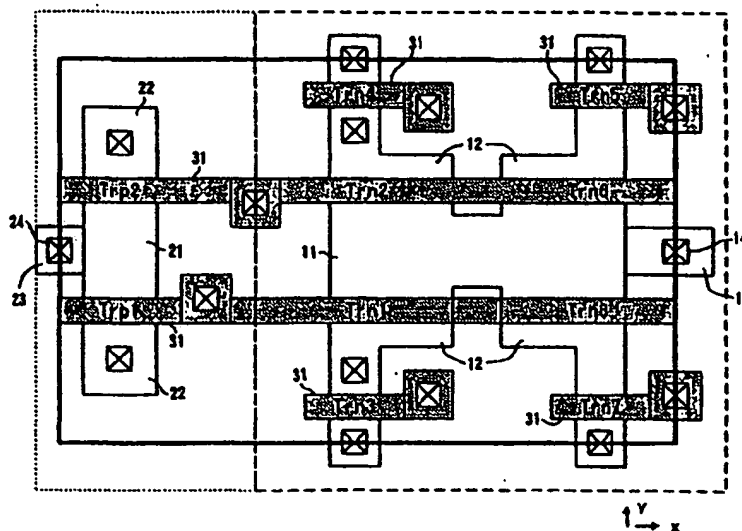
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(54) **Semiconductor memory device**

(57) A semiconductor memory device is constructed to include a memory cell formed by a plurality of transis-

tors, wherein each of gate wiring layers of all of the transistors forming the memory cell is arranged to extend in one direction.

FIG.4



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EUROPEAN SEARCH REPORT

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Place of search	Date of completion of the search	Examiner	
Munich	29 November 2004	Blackley, W	
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**ANNEX TO THE EUROPEAN SEARCH REPORT
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